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PATENT NUMBER and
ISSUE DATE

U.S. UTILITY Patent Application

APPL NUM 10073196	FILING DATE 02/13/2002	CLASS 364	SUBCLASS 774	GAU 3827	EXAMINER J. J. J.
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**APPLICANTS: Tsunemasu Kimio;

**CONTINUING DATA VERIFIED:

None

** FOREIGN APPLICATIONS VERIFIED:
JAPAN 2001-034793 02/13/2001

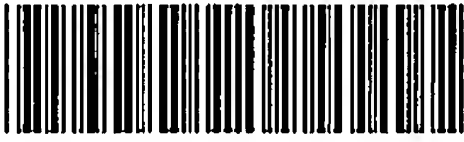
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PG-PUB	DO NOT PUBLISH <input type="checkbox"/>	RESCIND <input type="checkbox"/>
Foreign priority claimed <input checked="" type="checkbox"/> yes <input type="checkbox"/> no		ATTORNEY DOCKET NO 8037-1001
35 USC 119 conditions met <input type="checkbox"/> yes <input checked="" type="checkbox"/> no		
Verified and Acknowledged Examiners's Initials		
TITLE : Semiconductor device capable of preventing solder balls from being removed in reinforcing pad		

U.S. DEPT. OF COMM. PAT. & TM. PTO-434L (Rev. 12-94)

NOTICE OF ALLOWANCE MAILED		CLAIMS ALLOWED	
		Total Claims	Print Claim for O.G.
ISSUE FEE		DRAWING	
Amount Due	Date Paid	Sheets Drwg.	Figs. Drwg. Print Fig.
		Application Examiner	
<input type="checkbox"/> TERMINAL DISCLAIMER		PREPARED FOR ISSUE	
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Issue Classification 	Application No.	Applicant(s)	
	10/073,196	TSUNEMASU, KIMIO	
	Examiner	Art Unit	
	James M. Mitchell	2827	

ISSUE CLASSIFICATION									
ORIGINAL				CROSS REFERENCE(S)					
CLASS		SUBCLASS		CLASS	SUBCLASS (ONE SUBCLASS PER BLOCK)				
257		780		257	772	777	778	779	
INTERNATIONAL CLASSIFICATION									
H	01L	29140							
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 (Assistant Examiner) (Date)				KAMAND CUNEO SUPERVISORY PATENT EXAMINER TECHNOLOGY CENTER 2800  (Primary Examiner) (Date) 3/18/04				Total Claims Allowed:	
								O.G. Print Claim(s)	O.G. Print Fig.
								8	SA, SB

<input checked="" type="checkbox"/> Claims renumbered in the same order as presented by applicant					<input type="checkbox"/> CPA					<input type="checkbox"/> T.D.					<input type="checkbox"/> R.1.47				
Final	Original		Final	Original		Final	Original		Final	Original		Final	Original		Final	Original			
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